



# DVETR2800S Series

## HIGH RELIABILITY HYBRID DC-DC CONVERTERS WITH INTEGRAL EMI FILTER

### DESCRIPTION

The DVETR series of high-reliability DC-DC converters provide decades of successful critical mission support. Operable over the full military (-55 °C to +125 °C) temperature range with no power derating, the DVETR series unique input, and output filters dramatically reduce input and output noise performance. While operating at a nominal fixed frequency of 450 kHz, these regulated, isolated units utilize a high-speed magnetic feedback design and well-controlled under-voltage lockout circuitry to eliminate slow start-up problems.

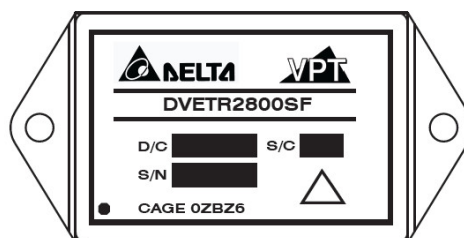
These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266  
5,790,389  
5,963,438  
5,999,433  
6,005,780  
6,084,792  
6,118,673

### FEATURES

- High Reliability
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Up to 40 Watts Output Power
- Fault Tolerant Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Industry Standard Pinout
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Precision Seam Welded Hermetic Package
- High Power Density: > 30 W/in<sup>3</sup>
- Custom Versions Available
- Additional Environmental Screening Available
- No External EMI Filter Required
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements
- Protects Against Conducted Susceptibility Specified in MIL-STD-461C, CS01 and CS02
- Flanged and Non-flanged Versions Available
- MIL-PRF-38534 Element Evaluated Components



**Figure 1** – DVETR2800S / DVETR2800SF DC-DC Converter  
(Exact marking may differ from that shown)

## SPECIFICATIONS (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS			
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	40 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	14 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(56 / 60) Grams

Parameter	Conditions	DVETR283R3S			DVETR2805S			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	15	28	50	15	28	50	V
	Transient, 1 sec	-	-	80	-	-	80	V
Current	Inhibited	-	-	6	-	-	6	mA
	No Load	-	-	90	-	-	90	mA
Inhibit Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltage <sup>4</sup>		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On		12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off <sup>4</sup>		11.0	-	14.5	11.0	-	14.5	V
OUTPUT Voltage	V <sub>OUT</sub> T <sub>CASE</sub> = 25°C	3.267	3.30	3.333	4.95	5.00	5.05	V
	V <sub>OUT</sub> T <sub>CASE</sub> = -55°C to +125°C	3.25	3.30	3.35	4.90	5.00	5.10	V
Power <sup>3</sup>		0	-	20	0	-	30	W
Current <sup>3</sup>	V <sub>OUT</sub>	0	-	6.06	0	-	6.0	A
Ripple Voltage	V <sub>OUT</sub> Full Load, 20Hz to 10MHz	-	-	50	-	-	50	mV <sub>p-p</sub>
Line Regulation	V <sub>OUT</sub> V <sub>IN</sub> = 15V to 50V	-	-	20	-	-	20	mV
Load Regulation	V <sub>OUT</sub> No Load to Full Load	-	-	50	-	-	50	mV
EFFICIENCY		65	-	-	70	-	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	16	-	-	16	W
	Short Circuit	-	-	16	-	-	16	W
CAPACITIVE LOAD <sup>4</sup>		-	-	1000	-	-	1000	μF
SWITCHING FREQUENCY		400	475	550	450	500	550	kHz
ISOLATION	500 V <sub>DC</sub>	100	-	-	100	-	-	MΩ
MTBF (MIL-HDBK-217F)	AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs
<b>DYNAMIC</b>								
Load Step Output Transient	V <sub>OUT</sub> Half Load to Full Load	-	-	400	-	-	500	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>		-	-	500	-	-	500	μSec
Line Step Output Transient <sup>4</sup>	V <sub>OUT</sub> V <sub>IN</sub> = 16V to 40V	-	150	600	-	150	600	mV <sub>PK</sub>
Line Step Recovery <sup>2, 4</sup>		-	100	500	-	100	500	μSec
Turn On Delay	V <sub>OUT</sub> V <sub>IN</sub> = 0V to 28V	-	-	20	-	-	20	mSec
Turn On Overshoot		-	-	30	-	-	50	mV <sub>PK</sub>

Notes:

1. Dependent on output voltage.
2. Time for output voltage to settle within 1% of its nominal value.
3. Derate linearly to 0 at 135°C.
4. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.

## SPECIFICATIONS (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load, Unless Otherwise Specified)

### ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	40 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	14 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(56 / 60) Grams

Parameter	Conditions	DVETR2812S			DVETR2815S			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	15	28	50	15	28	50	V
	Transient, 1 sec	-	-	80	-	-	80	V
Current	Inhibited	-	-	6	-	-	6	mA
	No Load	-	-	90	-	-	90	mA
Inhibit Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltage <sup>4</sup>		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On		12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off <sup>4</sup>		11.0	-	14.5	11.0	-	14.5	V
OUTPUT Voltage	V <sub>OUT</sub> T <sub>CASE</sub> = 25°C	11.88	12.0	12.12	14.85	15.0	15.15	V
	V <sub>OUT</sub> T <sub>CASE</sub> = -55°C to +125°C	11.72	12.0	12.18	14.775	15.0	15.225	V
Power <sup>3</sup>		0	-	40	0	-	40	W
Current <sup>3</sup>	V <sub>OUT</sub>	0	-	3.33	0	-	2.67	A
Ripple Voltage	V <sub>OUT</sub> Full Load, 20Hz to 10MHz	-	-	50	-	-	50	mV <sub>p-p</sub>
Line Regulation	V <sub>OUT</sub> V <sub>IN</sub> = 15V to 50V	-	-	20	-	-	20	mV
Load Regulation	V <sub>OUT</sub> No Load to Full Load	-	-	50	-	-	50	mV
EFFICIENCY		74	-	-	75	-	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	16	-	-	16	W
	Short Circuit	-	-	16	-	-	16	W
CAPACITIVE LOAD <sup>4</sup>		-	-	500	-	-	500	μF
SWITCHING FREQUENCY		400	475	550	400	475	550	kHz
ISOLATION	500 V <sub>DC</sub>	100	-	-	100	-	-	MΩ
MTBF (MIL-HDBK-217F)	AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs
<b>DYNAMIC</b>								
Load Step Output Transient	V <sub>OUT</sub> Half Load to Full Load	-	-	700	-	-	800	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>		-	-	500	-	-	500	μSec
Line Step Output Transient <sup>4</sup>	V <sub>OUT</sub> V <sub>IN</sub> = 16V to 40V	-	800	1200	-	800	1200	mV <sub>PK</sub>
Line Step Recovery <sup>2, 4</sup>		-	300	500	-	300	500	μSec
Turn On Delay	V <sub>OUT</sub> V <sub>IN</sub> = 0V to 28V	-	-	20	-	-	20	mSec
Turn On Overshoot		-	-	50	-	-	50	mV <sub>PK</sub>

#### Notes:

1. Dependent on output voltage.
2. Time for output voltage to settle within 1% of its nominal value.
3. Derate linearly to 0 at 135°C.
4. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.

## SPECIFICATIONS (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS			
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	40 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	14 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(56 / 60) Grams

Parameter	Conditions	DVETR285R2S			DVETR285R7S			Units
		Min	Typ	Max	Min	Typ	Max	
<b>STATIC</b>								
INPUT Voltage <sup>4</sup>	Continuous	15	28	50	15	28	50	V
	Transient, 1 sec	-	-	80	-	-	80	V
Current	Inhibited	-	-	6	-	-	6	mA
	No Load	-	-	90	-	-	90	mA
Inhibit Pin Input <sup>4</sup>		0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Voltage <sup>4</sup>		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On		12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off <sup>4</sup>		11.0	-	14.5	11.0	-	14.5	V
OUTPUT Voltage	V <sub>OUT</sub> T <sub>CASE</sub> = 25°C	5.14	5.20	5.26	5.64	5.70	5.76	V
	V <sub>OUT</sub> T <sub>CASE</sub> = -55°C to +125°C	5.09	5.20	5.31	5.58	5.70	5.82	V
Power <sup>3</sup>		0	-	30	0	-	30	W
Current <sup>3</sup>	V <sub>OUT</sub>	0	-	5.8	-	-	5.3	A
Ripple Voltage	V <sub>OUT</sub> Full Load, 20Hz to 10MHz	-	-	50	-	-	50	mV <sub>p-p</sub>
Line Regulation	V <sub>OUT</sub> V <sub>IN</sub> = 15V to 50V	-	-	20	-	-	20	mV
Load Regulation	V <sub>OUT</sub> No Load to Full Load	-	-	50	-	-	50	mV
EFFICIENCY		70	-	-	70	77	-	%
LOAD FAULT POWER DISSIPATION	Overload <sup>4</sup>	-	-	16	-	-	16	W
	Short Circuit	-	-	16	-	-	16	W
CAPACITIVE LOAD <sup>4</sup>		-	-	1000	-	-	1000	μF
SWITCHING FREQUENCY		400	500	550	400	500	550	kHz
ISOLATION	500 V <sub>DC</sub>	100	-	-	100	-	-	MΩ
MTBF (MIL-HDBK-217F)	AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs
<b>DYNAMIC</b>								
Load Step Output Transient	V <sub>OUT</sub> Half Load to Full Load	-	-	500	-	-	500	mV <sub>PK</sub>
Load Step Recovery <sup>2</sup>		-	-	500	-	-	500	μSec
Line Step Output Transient <sup>4</sup>	V <sub>OUT</sub> V <sub>IN</sub> = 16V to 40V	-	150	600	-	-	600	mV <sub>PK</sub>
Line Step Recovery <sup>2, 4</sup>		-	100	500	-	-	500	μSec
Turn On Delay	V <sub>OUT</sub> V <sub>IN</sub> = 0V to 28V	-	-	20	-	-	20	mSec
Turn On Overshoot		-	-	50	-	-	50	mV <sub>PK</sub>

Notes:

1. Dependent on output voltage.
2. Time for output voltage to settle within 1% of its nominal value.
3. Derate linearly to 0 at 135°C.
4. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.

**BLOCK DIAGRAM**

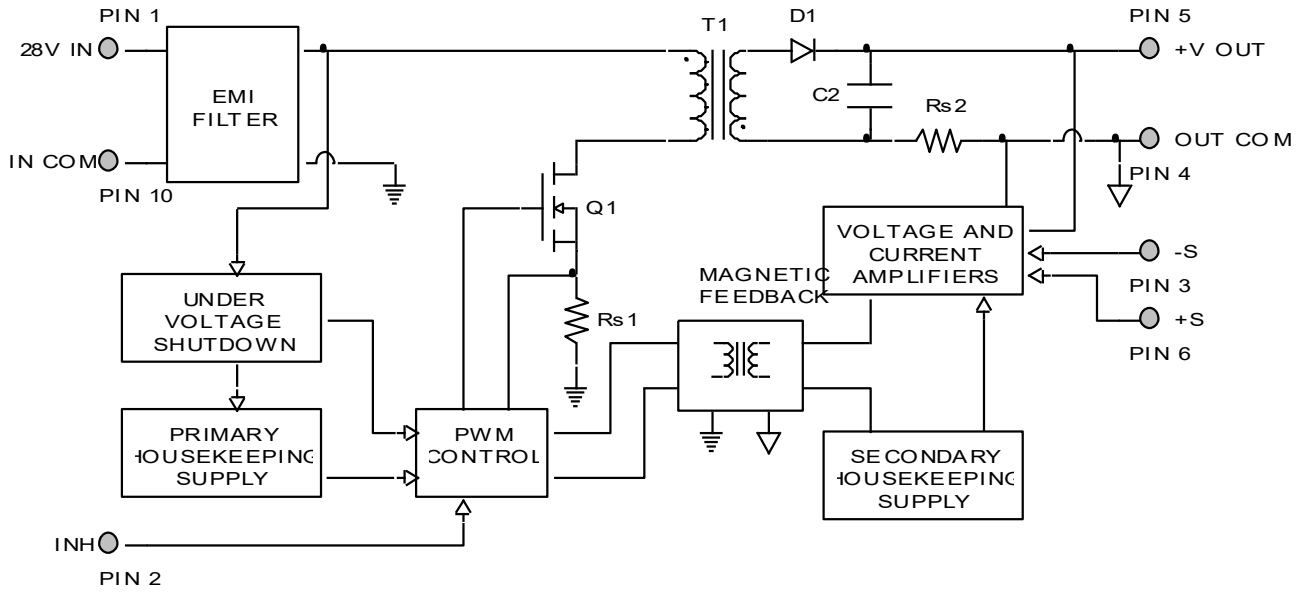


Figure 2

**CONNECTION DIAGRAM**

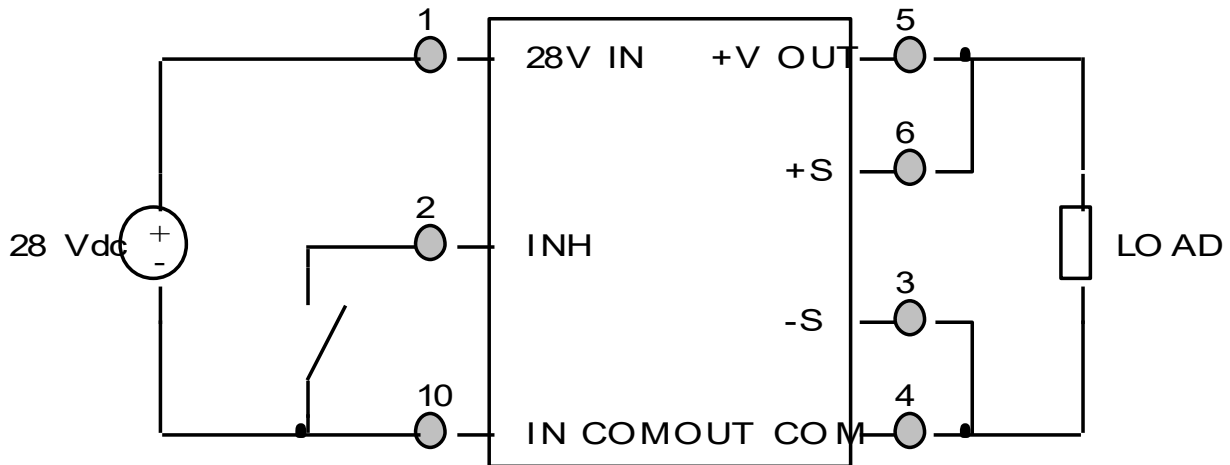


Figure 3

**INHIBIT DRIVE CONNECTION DIAGRAMS**

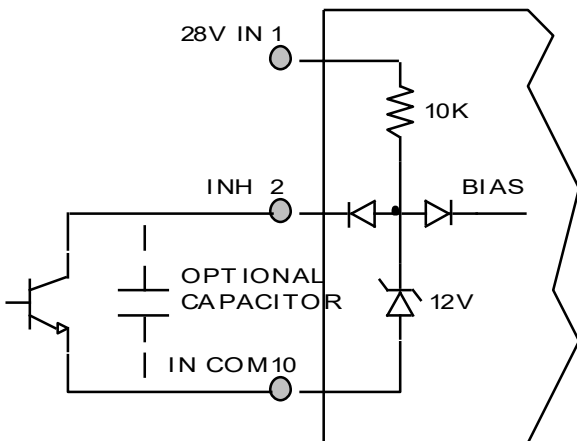


Figure 4 – Internal Inhibit Circuit and Recommended Drive  
(Shown with optional capacitor for turn-on delay)

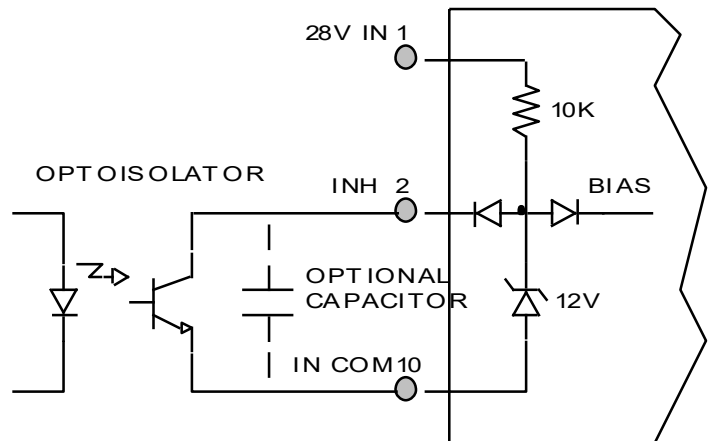
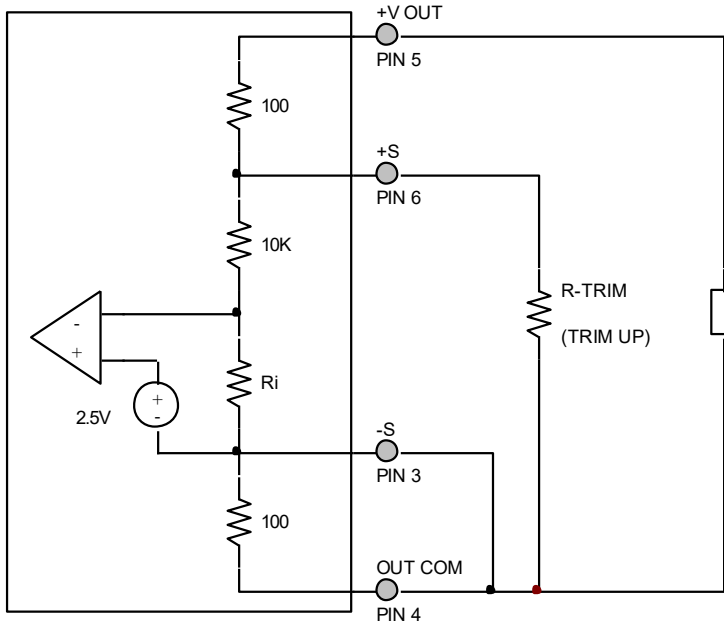


Figure 5 – Isolated Inhibit Drive  
(Shown with optional capacitor for turn-on delay)

**OUTPUT VOLTAGE TRIM**



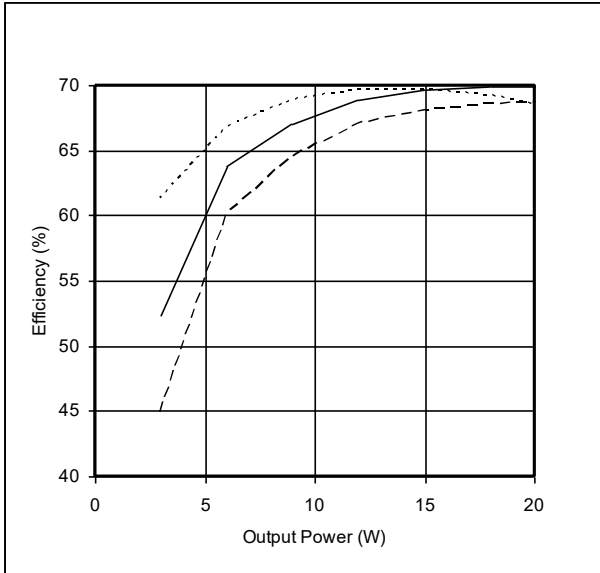
The output voltage can be trimmed up by connecting a resistor between the +S pin (PIN 6) and the OUT COM pin (PIN 4). The maximum trim range is +10%. The appropriate resistor values versus the output voltage are given in the trim table below. The -S pin should be connected to the OUT COM pin.

**Figure 6 – Output Voltage Trim**

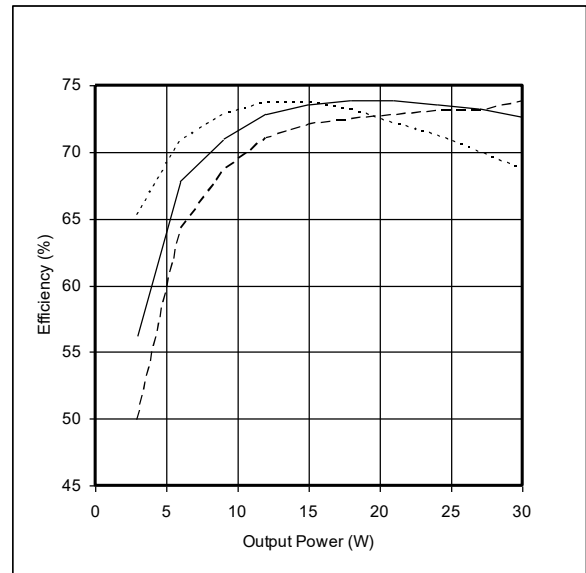
DVETR283R3S		DVETR2805S		DVETR285R2S		DVETR285R7S		DVETR2812S		DVETR2815S	
+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)
3.60	1.13k	5.50	1.05k	5.70	1.09k	6.25	1.1k	13.2	1.09k	16.50	1.09k
3.55	1.36k	5.45	1.18k	5.65	1.22k	6.20	1.22k	13.1	1.19k	16.40	1.18k
3.50	1.72k	5.40	1.33k	5.60	1.39k	6.15	1.36k	13.0	1.33k	16.30	1.28k
3.45	2.32k	5.35	1.54k	5.55	1.60k	6.10	1.55k	12.9	1.49k	16.20	1.4k
3.40	3.59k	5.30	1.82k	5.50	1.89k	6.05	1.79k	12.8	1.7k	16.10	1.54k
3.35	7.87k	5.25	2.22k	5.45	2.31k	6.00	2.13k	12.7	1.98k	16.00	1.71k
3.30	-	5.20	2.86k	5.40	2.97k	5.95	2.62k	12.6	2.38k	15.90	1.94k
		5.15	4k	5.35	4.16k	5.90	3.39k	12.5	2.96k	15.80	2.22k
		5.10	6.67k	5.30	6.93k	5.85	4.83k	12.4	3.94k	15.70	2.61k
		5.05	20k	5.25	20.8k	5.80	8.38k	12.3	5.86k	15.60	3.16k
		5.00	-	5.20	-	5.75	31.7k	12.2	11.4k	15.50	4k
						5.70	-	12.1	242k	15.40	5.46k
								12.0	-	15.30	8.57k
										15.20	20k
										15.10	Note 1
										15.00	-

Notes: 1. Connect R-TRIM = 400Ω from +VOUT (PIN 5) to +S (PIN 6).

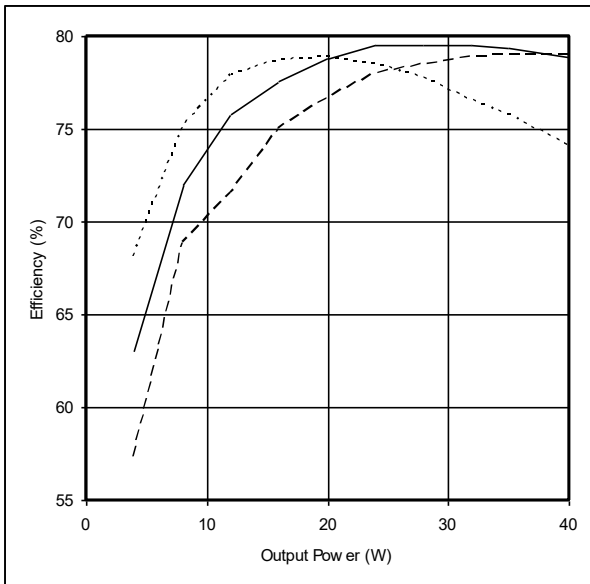
**EFFICIENCY PERFORMANCE CURVES** ( $T_{CASE} = 25^{\circ}C$ , Full Load, Unless Otherwise Specified)



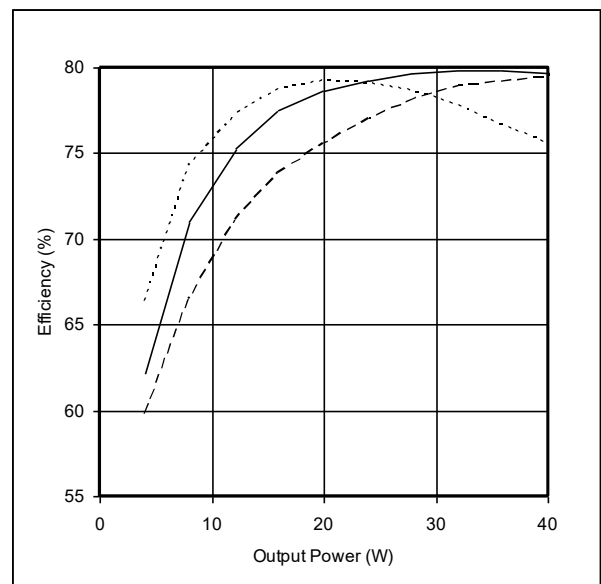
**Figure 7 – DVETR283R3S**  
Efficiency (%) vs. Output Power (W)



**Figure 8 – DVETR2805S / DVETR285R2S**  
Efficiency (%) vs. Output Power (W)



**Figure 9 – DVETR2812S**  
Efficiency (%) vs. Output Power (W)

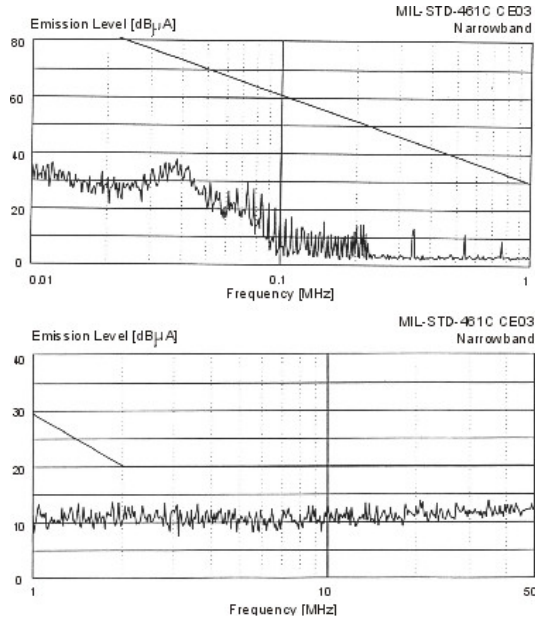


**Figure 10 – DVETR2815S**  
Efficiency (%) vs. Output Power (W)

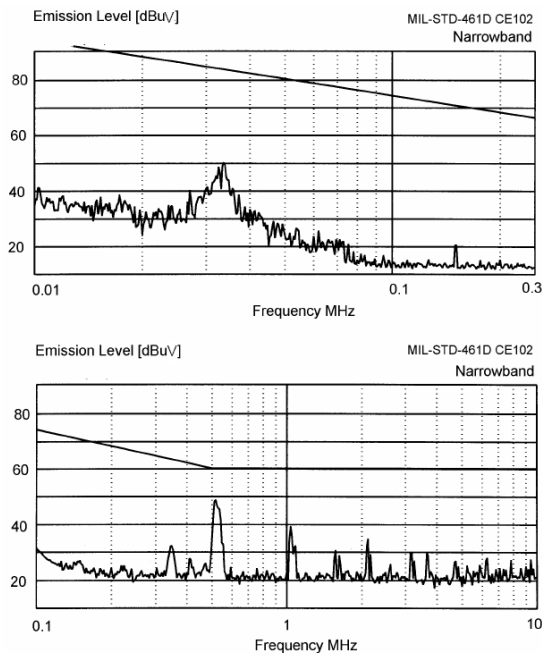


**EMI PERFORMANCE CURVES**

(T<sub>CASE</sub> = 25°C, V<sub>IN</sub> = +28V ± 5%, Full Load, Unless Otherwise Specified)

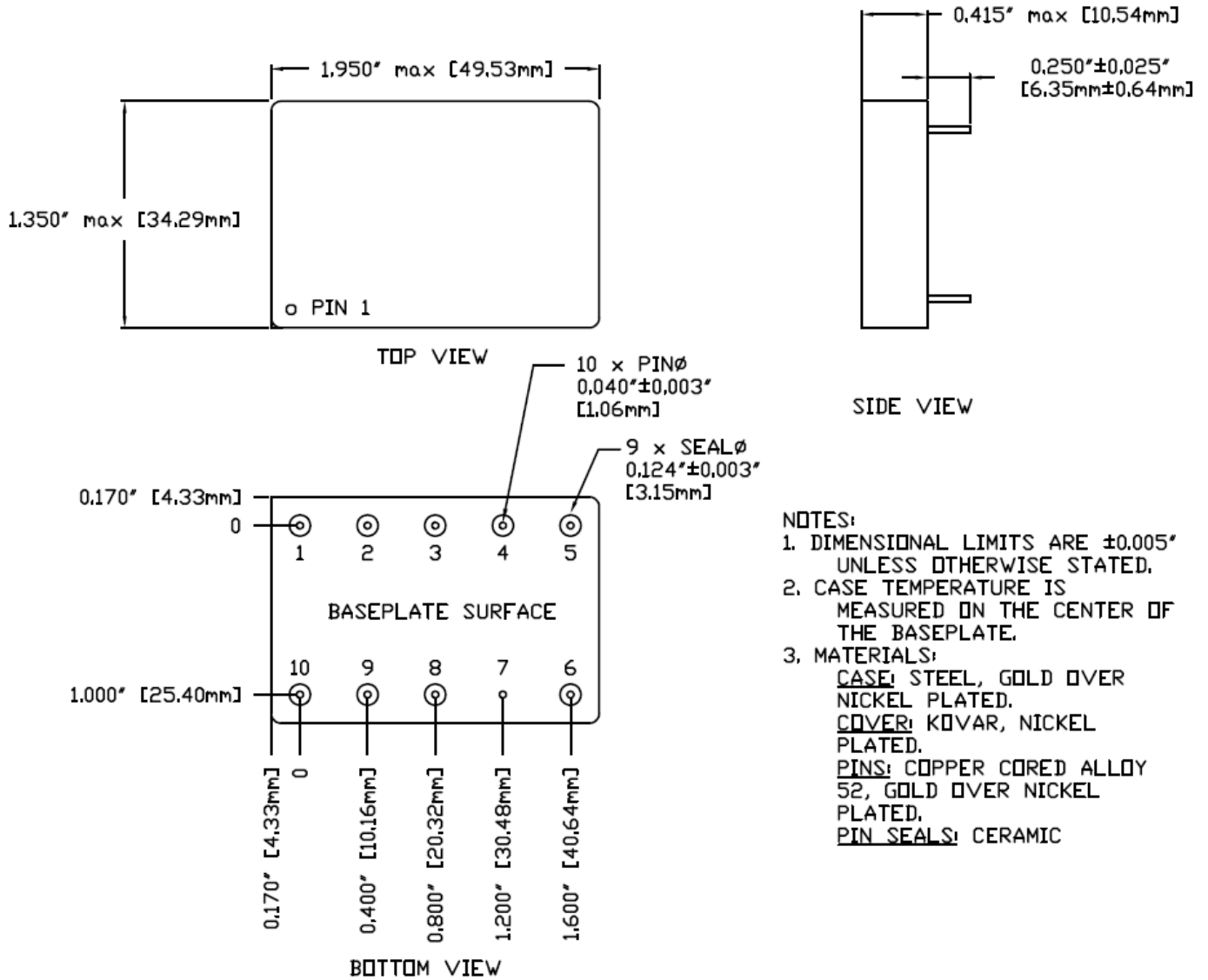


**Figure 11 – MIL-STD-461C  
DVETR2800S**



**Figure 12 – MIL-STD-461D  
DVETR2800S**

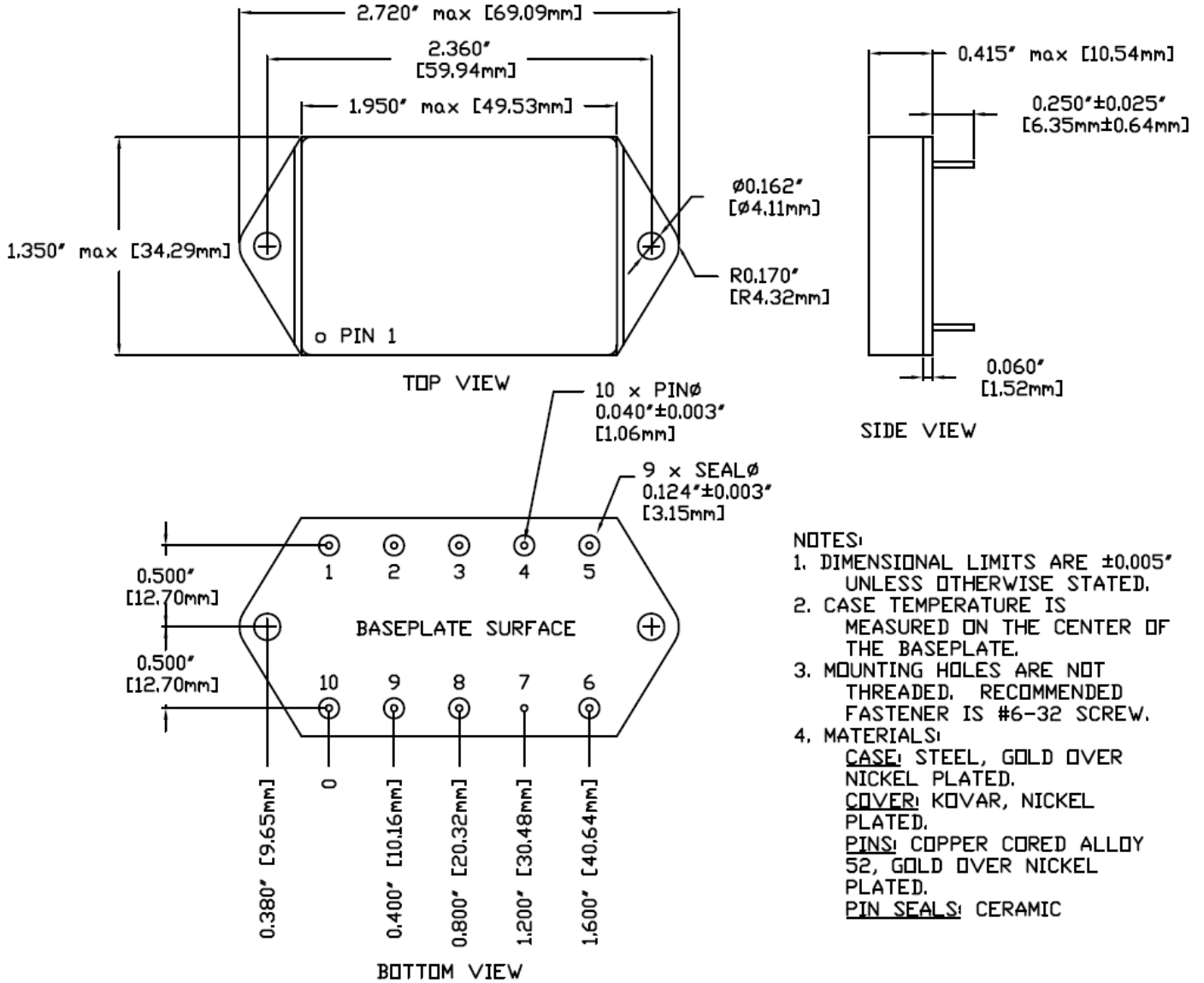
**PACKAGE SPECIFICATIONS (NON-FLANGED, SEAM SEAL)**



Pin	Function	Pin	Function
1	28V IN	6	+S
2	INHIBIT	7	CASE
3	-S	8	N/C
4	OUT COM	9	N/C
5	+V OUT	10	IN COM

Figure 13 – Non-Flanged, Seam Seal Package and Pinout

**PACKAGE SPECIFICATIONS (FLANGED, SEAM SEAL)**



Pin	Function	Pin	Function
1	28V IN	6	+S
2	INHIBIT	7	CASE
3	-S	8	N/C
4	OUT COM	9	N/C
5	+V OUT	10	IN COM

**Figure 14** – Flanged, Seam Seal Package and Pinout

## PACKAGE PIN DESCRIPTION

Pin	Function	Description
1	28V IN	Positive Input Voltage Connection
2	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown. Logic High = Enabled Output. Unconnected or open collector TTL.
3	-S	Return Sense
4	OUT COM	Output Common Connection
5	+V OUT	Positive Output Voltage Connection
6	+S	Positive Sense
7	CASE	Case Connection
8	N/C	No Connection
9	N/C	No Connection
10	IN COM	Input Common Connection

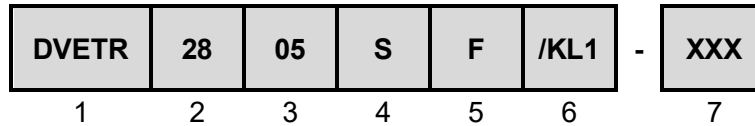
## ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

Test	MIL-STD-883 Test Method, Condition	No Suffix (Standard) Non-QML 5	/ES (Extended) Non-QML 5	/H (Class H)	/KL1 Non-QML 5,9
Non-Destructive Bond Pull	TM2023	• <sup>4</sup>	• <sup>4</sup>	• <sup>4</sup>	•
Internal Visual	TM2010, TM2017, TM2032 (MIL-STD-750, TM2072, TM2073)	•	•	•	•
Temperature Cycling	TM1010, Condition C -65°C to 150°C, Ambient			•	•
	TM1010, Condition B -55°C to 125°C, Ambient		•		
Constant Acceleration	TM2001, 3000g, Y1 Direction			•	•
	TM2001, 500g, Y1 Direction		•		
PIND <sup>7</sup>	TM2020, Condition A				•
Pre Burn-In Electrical	25°C				•
Burn-In	TM1015, 320 hrs, 125°C, Case Typ				•
	TM1015, 160 hrs, 125°C, Case Typ			•	
	96 hrs, 125°C, Case Typ		•		
	24 hrs, 125°C, Case Typ	•			
Final Electrical	MIL-PRF-38534, Group A Subgroups 1-6 -55°C, 25°C, 125°C <sup>3</sup>			•	•
	MIL-PRF-38534, Group A Subgroups 1 and 4 25°C	•	•		
Hermeticity (Seal)	TM1014, Fine Leak, Condition A2 or B1		•	•	•
	TM1014, Gross Leak, Condition C1 or B2		•	•	•
	Gross Leak, Dip (No Bomb), Visual Verification	•			
Radiography <sup>8</sup>	TM2012				•
External Visual	TM2009	•	•	•	•

Notes:

- Contact Sales for more information concerning additional environmental screening and testing options desired.
- VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.
- 100% R&R testing with all test data included in product shipment.
- Not required per MIL-PRF-38534. Test is performed for additional product quality assurance.
- Non-QML products may not meet all requirements of MIL-PRF-38534.
- Note intentionally not used.
- PIND test Certificate of Compliance included in product shipment.
- Radiographic test Certificate of Compliance and film(s) or data CD included in product shipment.
- KL1 products are identical in every way with Class K products in compliance with MIL-PRF-38534 revision L and later revisions except they contain elements evaluated to the requirements of MIL-PRF-38534 revision K and previous revisions. These devices are not marked with an SMD number or MIL-PRF-38534 certification mark and are marked with -KL1 screening code in place of -K.

## ORDERING INFORMATION



(1)

(2)

(3)

(4)

Product Series	Nominal Input Voltage		Output Voltage		Number of Outputs	
DVETR	28	28 Volts	<b>3R3</b> <b>05</b> <b>5R2</b> <b>5R7</b> <b>12</b> <b>15</b>	3.3 Volts 5 Volts 5.2 Volts 5.7 Volts 12 Volts 15 Volts	S	Single

(5)

(6)

(7)

Package Option		Screening Code <sup>1</sup>		Additional Screening Code
<b>None</b> <b>F</b>	Non-Flanged Flanged	<b>None</b> <b>/ES</b> <b>/H</b> <b>/KL1</b>	Standard Extended Class H Class KL1	Contact Sales

- Notes:
1. Contact the VPT Inc. Sales Department for availability of Class H (/H) and KL1 (/KL1) qualified products.
  2. VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.
  3. -KL1 products are identical in every way with Class K products in compliance with MIL-PRF-38534 revision L and later revisions except they contain elements evaluated to the requirements of MIL-PRF-38534 revision K and previous revisions. These devices are not marked with an SMD number or MIL-PRF-38534 certification mark and are marked with -KL1 screening code in place of -K.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.

## SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS

Standard Microcircuit Drawing (SMD)	DVETR2800S Series Similar Part Number	Standard Microcircuit Drawing (SMD)	DVETR2800S Series Similar Part Number
5962-1520801HXC	DVETR283R3S/H	5962-1520804HXC	DVETR285R7S/H
5962-1520801HXA	DVETR283R3S/H-E	5962-1520804HXA	DVETR285R7S/H-E
5962-1520801HYC	DVETR283R3SF/H	5962-1520804HYC	DVETR285R7SF/H
5962-1520801HYA	DVETR283R3F/H-E	5962-1520804HYA	DVTER285R7S/H-E
5962-1520802HXC	DVETR2805S/H	5962-1520805HXC	DVETR2812S/H
5962-1520802HXA	DVETR2805S/H-E	5962-1520805HXA	DVETR2812S/H-E
5962-1520802HYC	DVETR2805SF/H	5962-1520805HYC	DVETR2812SF/H
5962-1520802HYA	DVETR2805SF/H-E	5962-1520805HYA	DVETR2812SF/H-E
5962-1520803HXC	DVETR285R2S/H	5962-1520806HXC	DVETR2815S/H
5962-1520803HXA	DVETR285R2S/H-E	5962-1520806HXA	DVETR2815S/H-E
5962-1520803HYC	DVETR285R2SF/H	5962-1520806HYC	DVETR2815SF/H
5962-1520803HYA	DVTER285R2S/H-E	5962-1520806HYA	DVETR2815SF/H-E

Do not use the DVETR2800S Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMD's can be downloaded from the DLA Land and Maritime (Previously known as DSCC) website at <https://landandmaritimeapps.dla.mil/programs/defaultapps.asp>. The SMD number listed above is for MIL-PRF-38534 Class H screening, standard gold plated lead finish, and no RHA (Radiation Hardness Assurance) level. Please reference the SMD for other screening levels, lead finishes, and radiation levels. All SMD products are marked with a "Q" on the cover as specified by the QML certification mark requirement of MIL-PRF-38534.

### CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

**Phone:** (425) 353-3010  
**Fax:** (425) 353-4030  
**E-mail:** [vptsales@vptpower.com](mailto:vptsales@vptpower.com)

All information contained in this datasheet is believed to be accurate, however, no responsibility is assumed for possible errors or omissions. The products or specifications contained herein are subject to change without notice.