

# HIGH RELIABILITY HYBRID DC-DC CONVERTERS WITH INTEGRAL EMI FILTER

#### **DESCRIPTION**

The DVETR series of high-reliability DC-DC converters provide decades of successful critical mission support. Operable over the full military (-55 °C to +125 °C) temperature range with no power derating, the DVETR series unique input, and output filters dramatically reduce input and output noise performance. While operating at a nominal fixed frequency of 450 kHz, these regulated, isolated units utilize a high-speed magnetic feedback design and well-controlled under-voltage lockout circuitry to eliminate slow start-up problems.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433 6,005,780 6,084,792 6,118,673

#### **FEATURES**

- High Reliability
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Up to 40 Watts Output Power
- Fault Tolerant Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Industry Standard Pinout
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Precision Seam Welded Hermetic Package
- High Power Density: > 30 W/in<sup>3</sup>
- Custom Versions Available
- Additional Environmental Screening Available
- No External EMI Filter Required
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements
- Protects Against Conducted Susceptibility Specified in MIL-STD-461C, CS01 and CS02
- Flanged and Non-flanged Versions Available
- MIL-PRF-38534 Element Evaluated Components



Figure 1 – DVETR2800D / DVETR2800DF DC-DC Converter (Exact marking may differ from that shown)

Sales Information: Phone: (425) 353-3010 Fax: (425) 353-4030 E-mail: vptsales@vptpower.com



**SPECIFICATIONS** (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

**ABSOLUTE MAXIMUM RATINGS** 

 $\begin{array}{lll} \mbox{Input Voltage (Continuous)} & 50 \ \mbox{V}_{DC} \\ \mbox{Input Voltage (Transient, 1 second)} & 80 \ \mbox{Volts} \\ \mbox{Output Power}^1 & 40 \ \mbox{Watts} \\ \mbox{Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}$C)} & 14 \ \mbox{Watts} \\ \end{array}$ 

Junction Temperature Rise to Case +15°C
Storage Temperature -65°C to +150°C

Lead Solder Temperature (10 seconds) 270°C

Weight (Maximum) (Un-Flanged / Flanged) (56 / 60) Grams

Parameter		Conditions	D	VETR2805	SD.	DVETR2812D			Heite
		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC					-	_		_	
INPUT		Continuous	15	28	50	15	28	50	V
Voltage <sup>4</sup>		Transient, 1 sec	-	-	80	-	-	80	V
Current		Inhibited	-	-	6	-	-	6	mA
Current		No Load	-	-	90	-	-	90	mA
Inhibit Pin Input⁴			0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit	Voltage⁴		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On			12.0	-	14.8	12	-	14.8	V
UVLO Turn Off <sup>4</sup>			11.0	-	14.5	11.0	-	14.5	V
	+V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	4.95	5.0	5.05	11.88	12.0	12.12	V
OUTPUT	+V <sub>OUT</sub>	T <sub>CASE</sub> = -55°C to +125°C	4.925	5.0	5.075	11.82	12.0	12.18	V
Voltage <sup>5</sup>	-V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	4.80	5.0	5.20	11.80	12.0	12.20	V
	-V <sub>OUT</sub>	T <sub>CASE</sub> = -55°C to +125°C	4.70	5.0	5.30	11.64	12.0	12.36	V
2.6	Total		0	-	30	0	-	40	W
Power <sup>3,6</sup>	$\pm V_{\text{OUT}}$	Either Output	0	-	21	0	-	28	W
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	0	-	4.2	0	-	2.33	Α
Ripple Voltage	$\pm V_{\text{OUT}}$	Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	60	-	-	50	mV <sub>p-p</sub>
5	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	20	-	-	20	mV
Line Regulation	-V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	200	-	-	200	mV
	+V <sub>OUT</sub>	No Load to Full Load⁵	-	-	50	-	-	50	mV
Load Regulation	-V <sub>out</sub>	No Load to Full Load⁵	-	-	200	-	-	200	mV
Cross Regulation	-V <sub>OUT</sub>	+Load 70%, -Load 30% +Load 30%, -Load 70%	-	-	650	-	-	650	mV
EFFICIENCY		Full Load <sup>5</sup>	70	-	-	74	-	-	%
		Overload <sup>4</sup>	-	-	16	-	-	14	W
LOAD FAULT POWER DISSIPATION		Short Circuit	-	-	16	-	-	14	W
CAPACITIVE LOAD <sup>4</sup> Eitl		Either Output	-	-	500	-	-	500	μF
SWITCHING FREQUENCY			400	500	550	400	500	550	kHz
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	100	-	-	МΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	413	-	-	413	-	kHrs

See notes next page.



**SPECIFICATIONS** (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS			
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	40 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	14 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(56 / 60) Grams

Parameter		Conditions	DVETR2805D			DVETR2812D			Units
		Conditions	Min	Тур	Max	Min	Тур	Max	Ullits
DYNAMIC									
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	500	-	-	600	$mV_{PK}$
Load Step Recovery <sup>2</sup>		Hall Load to Full Load	-	-	350	-	-	400	μSec
Line Step Output Transient4	±V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	150	600	-	850	1200	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>		V <sub>IN</sub> - 10V to 40V	-	150	500	-	300	500	μSec
Turn On Delay	±V <sub>OUT</sub>	\/ = 0\/ to 20\/	-	-	20	-	-	20	mSec
Turn On Overshoot		V <sub>IN</sub> = 0V to 28V	-	-	50	-	-	50	$mV_{PK}$

#### Notes:

- 1. Dependent on output voltage.
- 2. Time for output voltage to settle within 1% of its nominal value.
- 3. Derate linearly to 0 at 135°C.
- 4. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.
- 5. Half load at +VouT and half load at -VouT.
- 6. Up to 70% of the total power or current can be drawn from any one of the two outputs.

**SPECIFICATIONS** (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

**ABSOLUTE MAXIMUM RATINGS** Input Voltage (Continuous) 50 V<sub>DC</sub> Junction Temperature Rise to Case +15°C Input Voltage (Transient, 1 second) 80 Volts Storage Temperature -65°C to +150°C Output Power<sup>1</sup> 40 Watts Lead Solder Temperature (10 seconds) 270°C Power Dissipation (Full Load, T<sub>CASE</sub> = +125°C) Weight (Maximum) (Un-Flanged / Flanged) 14 Watts (56 / 60) Grams

Parameter		O a malifei a ma	D	VETR2815	SD.	1114
		Conditions	Min	Тур	Max	Units
STATIC					-	-
INPUT		Continuous	15	28	50	V
Voltage <sup>4</sup>		Transient, 1 sec	-	-	80	V
Current		Inhibited	-	-	6	mA
Current		No Load	-	-	90	mA
Inhibit Pin Input <sup>4</sup>			0	-	1.5	V
Inhibit Pin Open Circuit \	/oltage⁴		9.0	11.0	13.0	V
UVLO Turn On			12.0		14.8	V
UVLO Turn Off <sup>4</sup>			11.0	-	14.5	V
	+V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	14.85	15.0	15.15	V
OUTPUT	+V <sub>OUT</sub>	T <sub>CASE</sub> = -55°C to +125°C	14.70	15.0	15.30	V
Voltage <sup>5</sup>	-V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	14.70	15.0	15.30	V
	-V <sub>OUT</sub>	T <sub>CASE</sub> = -55°C to +125°C	14.55	15.0	15.45	V
D36	Total		-	-	40	W
Power <sup>3,6</sup>	$\pm V_{\text{OUT}}$	Either Output	-	1	28	W
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	-		1.87	Α
Ripple Voltage	±V <sub>OUT</sub>	Full Load <sup>5</sup> , 20Hz to 10MHz	-		50	$mV_{p-p}$
11 B 12	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	20	mV
Line Regulation	-V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	200	mV
1 15 16	+V <sub>OUT</sub>	No Load to Full Load <sup>5</sup>	-	-	50	mV
Load Regulation	-V <sub>OUT</sub>	No Load to Full Load <sup>5</sup>	-	-	200	mV
Cross Regulation	-V <sub>OUT</sub>	+Load 70%, -Load 30% +Load 30%, -Load 70%	-	-	650	mV
EFFICIENCY		Full Load <sup>5</sup>	75	-	-	%
		Overload <sup>4</sup>	-	-	14	W
LOAD FAULT POWER DISSIPATION		Short Circuit	-	-	14	W
CAPACITIVE LOAD <sup>4</sup>		Either Output	-	-	500	μF
SWITCHING FREQUENCY			400	500	550	kHz
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	413	-	kHrs

See notes next page.



**SPECIFICATIONS** (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS			
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	40 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	14 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(56 / 60) Grams

Parameter		Conditions	D	Units		
		Conditions	Min	Тур	Max	Ullits
DYNAMIC						
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	600	$mV_{PK}$
Load Step Recovery <sup>2</sup>		Hall Load to Full Load	-	-	300	μSec
Line Step Output Transient <sup>4</sup>	±V <sub>OUT</sub>	\/ = 16\/ to 10\/	-	850	1200	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>		$V_{IN} = 16V \text{ to } 40V$	-	300	500	μSec
Turn On Delay	±V <sub>OUT</sub>	\/ = 0\/ to 20\/	-	-	20	mSec
Turn On Overshoot		$V_{IN} = 0V \text{ to } 28V$	-	-	50	$mV_{PK}$

#### Notes:

- 1. Dependent on output voltage.
- 2. Time for output voltage to settle within 1% of its nominal value.
- 3. Derate linearly to 0 at 135°C.
- 4. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.
- 5. Half load at +VouT and half load at -VouT.
- 6. Up to 70% of the total power or current can be drawn from any one of the two outputs.



### **BLOCK DIAGRAM**

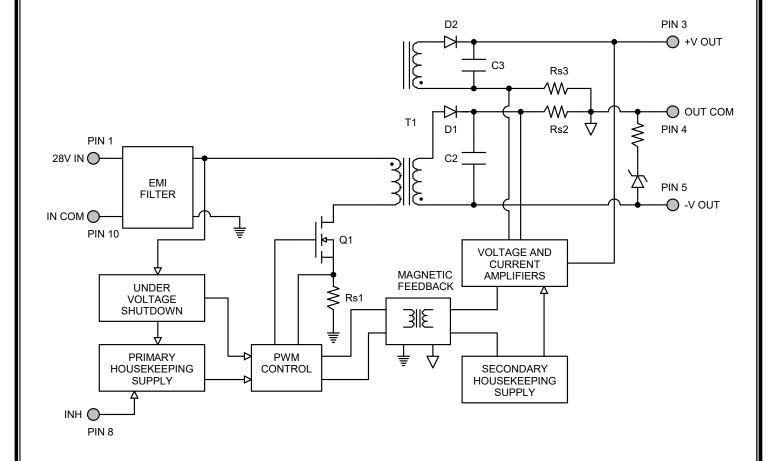


Figure 2



### **CONNECTION DIAGRAM**

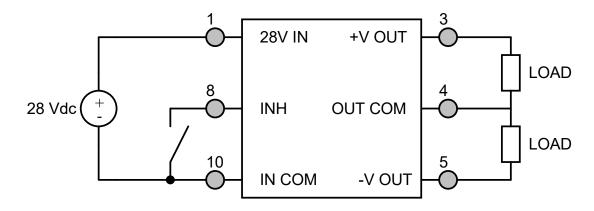
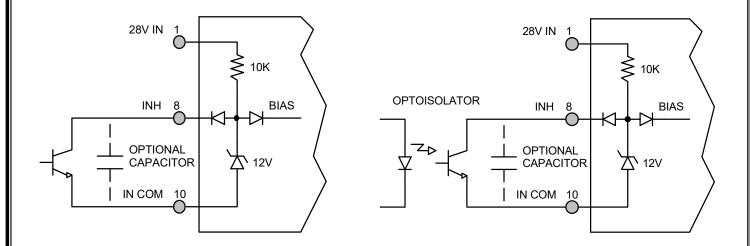


Figure 3

#### **INHIBIT DRIVE CONNECTION DIAGRAMS**



**Figure 4** – Internal Inhibit Circuit and Recommended Drive (Shown with optional capacitor for turn-on delay)

Figure 5 – Isolated Inhibit Drive (Shown with optional capacitor for turn-on delay)



### **EFFICIENCY PERFORMANCE CURVES** (T<sub>CASE</sub> = 25°C, Full Load, Unless Otherwise Specified)

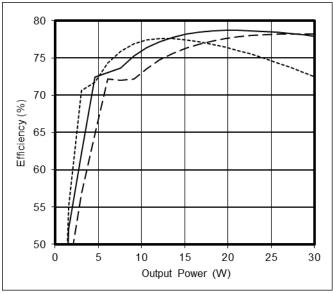


Figure 6 – DVETR2805D Efficiency (%) vs. Output Power (W)

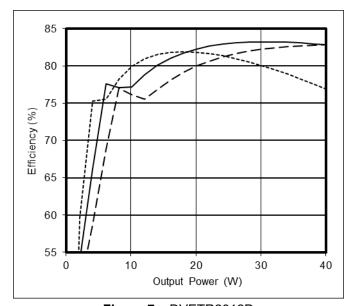


Figure 7 – DVETR2812D
Efficiency (%) vs. Output Power (W)

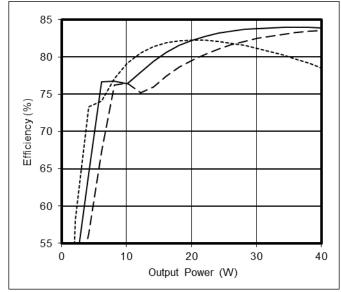


Figure 8 – DVETR2815D Efficiency (%) vs. Output Power (W)



CROSS REGULATION CURVES (T<sub>CASE</sub> = 25°C, Full Load, Unless Otherwise Specified)

+lo = 10% - - - +lo = 30% - +lo = 50% - +lo = 70%

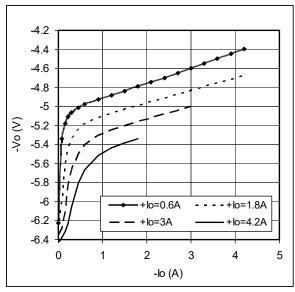


Figure 9 - DVETR2805D

-Vout (V) vs. -lout (A)

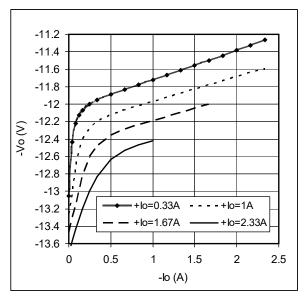
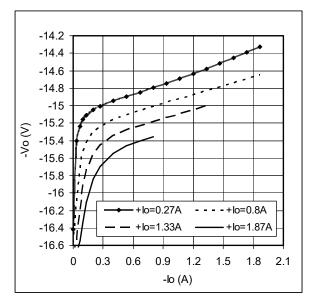


Figure 10 – DVETR2812D

-Vout (V) vs. -lout (A)



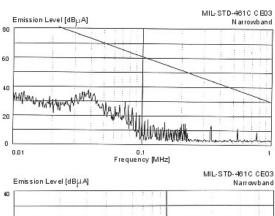
**Figure 11** – DVETR2815D

-Vout (V) vs. -lout (A)



#### **EMI PERFORMANCE CURVES**

(T<sub>CASE</sub> = 25°C, V<sub>IN</sub> = +28V ± 5%, Full Load, Unless Otherwise Specified)



Emission Level [dBµA]

Narrowband

10

10

Frequency [MHz]

Figure 12 – MIL-STD-461C DVETR2800D

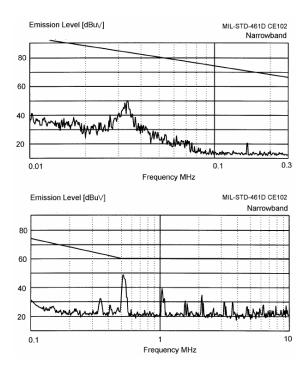
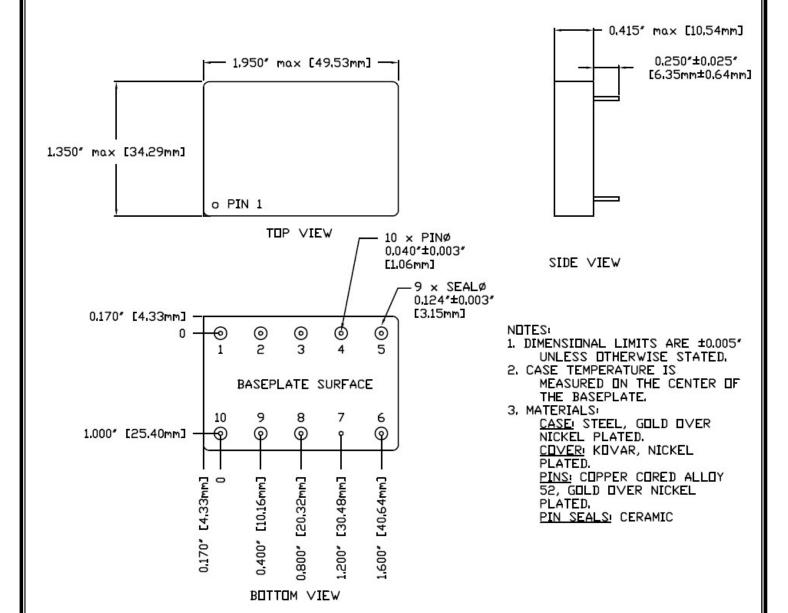


Figure 13 – MIL-STD-461D DVETR2800D



### PACKAGE SPECIFICATIONS (NON-FLANGED, SEAM SEAL)

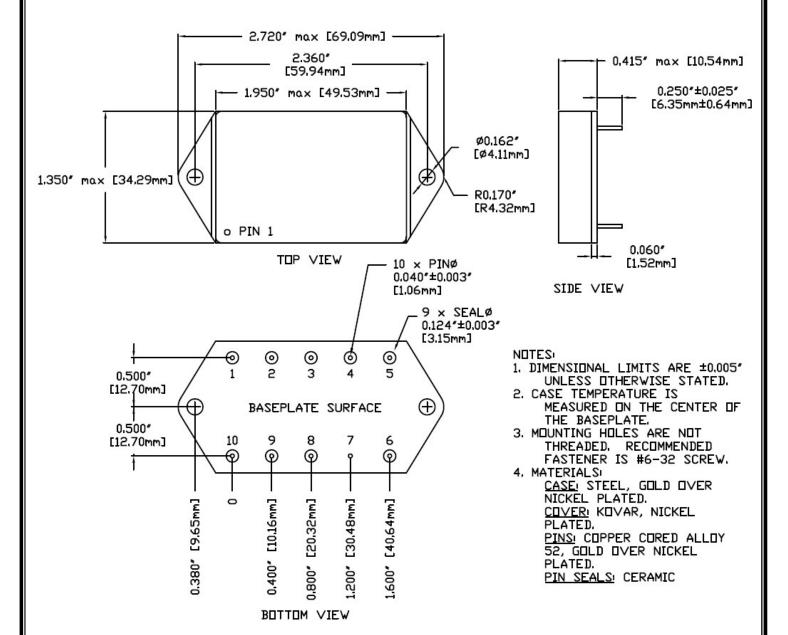


Pin	Function	Pin	Function
1	28V IN	6	N/C
2	N/C	7	CASE
3	+V OUT	8	INHIBIT
4	OUT COM	9	N/C
5	-V OUT	10	IN COM

Figure 14 - Non-Flanged, Seam Seal Package and Pinout



### PACKAGE SPECIFICATIONS (FLANGED, SEAM SEAL)



Pin	Function	Pin	Function
1	28V IN	6	N/C
2	N/C	7	CASE
3	+V OUT	8	INHIBIT
4	OUT COM	9	N/C
5	-V OUT	10	IN COM

Figure 15 - Flanged, Seam Seal Package and Pinout



## **PACKAGE PIN DESCRIPTION**

Pin	Function	Description
1	28V IN	Positive Input Voltage Connection
2	N/C	No Connection
3	+V OUT	Positive Output Voltage Connection
4	OUT COM	Output Common Connection
5	-V OUT	Negative Output Voltage Connection
6	N/C	No Connection
7	CASE	Case Connection
8	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown.  Logic High = Enabled Output. Unconnected or open collector TTL.
9	N/C	No Connection
10	IN COM	Input Common Connection



### **ENVIRONMENTAL SCREENING** (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

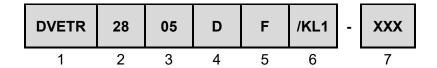
Test	MIL-STD-883 Test Method, Condition	No Suffix (Standard) Non-QML 5	/ES (Extended) Non-QML 5	/H (Class H)	/KL1 Non-QML 5,9
Non-Destructive Bond Pull	TM2023	•4	•4	•4	•
Internal Visual	TM2010, TM2017, TM2032 (MIL-STD-750, TM2072, TM2073)	•	•	•	•
Temperature Cycling	TM1010, Condition C -65°C to 150°C, Ambient TM1010, Condition B -55°C to 125°C, Ambient		•	•	•
Constant Acceleration	TM2001, 3000g, Y1 Direction TM2001, 500g, Y1 Direction		•	•	•
PIND <sup>7</sup>	TM2020, Condition A				•
Pre Burn-In Electrical	25°C				•
Burn-In	TM1015, 320 hrs, 125°C, Case Typ TM1015, 160 hrs, 125°C, Case Typ 96 hrs, 125°C, Case Typ 24 hrs, 125°C, Case Typ	•	•	•	•
Final Electrical	MIL-PRF-38534, Group A Subgroups 1-6 -55°C, 25°C, 125°C <sup>3</sup>			•	•
	MIL-PRF-38534, Group A Subgroups 1 and 4 25°C	•	•		
Hermeticity (Seal)	TM1014, Fine Leak, Condition A2 or B1 TM1014, Gross Leak, Condition C1 or B2 Gross Leak, Dip (No	•	•	•	•
Radiography <sup>8</sup>	Bomb), Visual Verification TM2012				•
External Visual	TM2009	•	•	•	•

#### Notes:

- 1. Contact Sales for more information concerning additional environmental screening and testing options desired.
- VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.
- 3. 100% R&R testing with all test data included in product shipment.
- 4. Not required per MIL-PRF-38534. Test is performed for additional product quality assurance.
- 5. Non-QML products may not meet all requirements of MIL-PRF-38534.
- Note intentionally not used.
- 7. PIND test Certificate of Compliance included in product shipment.
- 8. Radiographic test Certificate of Compliance and film(s) or data CD included in product shipment.
- 9. -KL1 products are identical in every way with Class K products in compliance with MIL-PRF-38534 revision L and later revisions except they contain elements evaluated to the requirements of MIL-PRF-38534 revision K and previous revisions. These devices are not marked with an SMD number or MIL-PRF-38534 certification mark and are marked with -KL1 screening code in place of -K.



#### ORDERING INFORMATION



 $(1) \qquad \qquad (2) \qquad \qquad (3)$ 

Product Series	Nominal Input Voltage		Output	Voltage	Number of Outputs		
DVETR	28	28 Volts	05 12 15	± 5 Volts ± 12 Volts ± 15 Volts	D	Dual	

(5) (6)(7) **Additional Screening** Screening Code<sup>1, 2, 3</sup> **Package Option** Code Non-Flanged None Standard **Contact Sales** None Flanged /ES Extended F /H Class H /KL1 Class KL1

Notes:

- 1. Contact the VPT Inc. Sales Department for availability of Class H (/H) and KL1 (/KL1) qualified products.
- 2. VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.
- 3. -KL1 products are identical in every way with Class K products in compliance with MIL-PRF-38534 revision L and later revisions except they contain elements evaluated to the requirements of MIL-PRF-38534 revision K and previous revisions. These devices are not marked with an SMD number or MIL-PRF-38534 certification mark and are marked with KL1 screening code in place of -K.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.



### **SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS**

Standard Microcircuit Drawing (SMD)	DVETR2800D Series Similar Part Number
5962-1520901HXC	DVETR2805D/H
5962-1520901HXA	DVETR2805D/H-E
5962-1520901HYC	DVETR2805DF/H
5962-1520901HYA	DVETR2805DF/H-E
5962-1520902HXC	DVETR2812D/H
5962-1520902HXA	DVETR2812D/H-E
5962-1520902HYC	DVETR2812DF/H
5962-1520902HYA	DVETR2812DF/H-E
5962-1520903HXC	DVETR2815D/H
5962-1520903HXA	DVETR2815D/H-E
5962-1520903HYC	DVETR2815DF/H
5962-1520903HYA	DVETR2815DF/H-E

Do not use the DVETR2800D Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMD's can be downloaded from the DLA Land and Maritime (Previously known as DSCC) website at <a href="https://landandmaritimeapps.dla.mil/programs/defaultapps.asp">https://landandmaritimeapps.dla.mil/programs/defaultapps.asp</a>. The SMD number listed above is for MIL-PRF-38534 Class H screening, standard gold plated lead finish, and no RHA (Radiation Hardness Assurance) level. Please reference the SMD for other screening levels, lead finishes, and radiation levels. All SMD products are marked with a "Q" on the cover as specified by the QML certification mark requirement of MIL-PRF-38534.



#### **CONTACT INFORMATION**

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

**Phone**: (425) 353-3010 **Fax**: (425) 353-4030

**E-mail**: vptsales@vptpower.com

All information contained in this datasheet is believed to be accurate, however, no responsibility is assumed for possible errors or omissions. The products or specifications contained herein are subject to change without notice.